

8-26-02

Sheet 1 of 1

INFORMATION DISCLOSURE STATEMENT		Atty Docket: Serial No.: Applicant: Filing Date: Group:	00GR35154360 10/082,816 COFLER ET AL. 02/25/2002
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U.S. PATENT DOCUMENTS							
Examiner Initials		Document Number	Date	Name	Class	Sub Class	Filing Date
W	AA	4,827,402	05/02/89	Wada	364	200	04/22/86
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Sub Class	Translation
W	AK	0 133 477	02/27/85	EP	G06F	9/38	
W	AL	0 689 131	12/27/95	EP	G06F	9/38	
	AM	0 840 209	05/06/98	EP	G06F	9/38	
	AN	1 050 805	11/08/00	EP	G06F	9/38	
W	AO	99/08184	02/18/99	WO	G06F	9/38	
	AP						

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

W	AQ	Moore C.R. "The Power PC™ 601 Microprocessor"; proceedings of the spring computer society international conference, San Francisco, CA published on February 22, 1993, Publication No. XP000379036; pages 109-116
W	AR	Toyohiko Yoshida et al., "A Strategy for Avoiding pipeline Interlock Delays in a Microprocessor", published by LSI Research and Development Laboratory on September 17, 1990; Publication No: XP 000201400; pages 14-19; ISBN: 0-8186-2079-X.
	AS	

EXAMINER: <i>Deny Jan</i>	DATE CONSIDERED: <i>8/2/04</i>
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	